

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re application of: ARONOWITZ et al.
Application No.: 10/698,169

Filed: October 31, 2003

Title: MEMORY DEVICE HAVING AN
ELECTRON TRAPPING LAYER IN

Attorney Docket No.:
02-6037/ LSI1P218

Examiner: MAI, Anh D.

Group: 2814

Confirmation No.: 9839

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on June 2, 2005 in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signed: Sue Funchess

Sue Funchess

**INFORMATION DISCLOSURE STATEMENT
AFTER FINAL ACTION OR NOTICE OF ALLOWANCE
(37 CFR §§ 1.56 AND 1.97(d))**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed after the mailing date of final action under §1.113 or a notice of allowance under §1.311, but before payment of the issue fee.

Accompanying this Information Disclosure Statement is the fee set forth in 37 CFR 1.17(p).

The undersigned hereby states:

- that each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of the Information Disclosure Statement, or
- that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

The Commissioner is authorized to charge the Information Disclosure Statement Fee of \$180.00 to Deposit Account 12-2252 (Order No. 02-6037).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP



Francis T. Kalinski II
Registration No. 44,177

P.O. Box 70250
Oakland, CA 94612-0250
Telephone: (650) 961-8300


Form 1449 (Modified)
**Information Disclosure
Statement By Applicant**

(Use Several Sheets if Necessary)

Atty Docket No.
02-6037/LSI1P218

Application No.:
10/698,169

Applicant:
ARONOWITZ et al.

Filing Date
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2814
U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	2001/0038135A	11/08/01	Forbes et al.			05/15/01
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	J							
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	Stanley Wolf et al., "Silicon Processing for the VLSI Era, Vol. 1: Process Technology", Lattice Press, Sunset Beach, CA, 1986, pg. 303-306.
	P	
	Q	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.